

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	133	graphics adj (chip processor) with processor with memory adj interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 12:12
L2	3	1 same advantage\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 12:12
L3	144	graphics adj (chip processor) with memory adj interface	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 12:12
L4	4	3 same advantage\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 12:37
L5	4	adjust\$4 near3 core adj clock same (delay dll\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 13:00
L6	23	synchroniz\$5 near3 core adj clock same (delay dll\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 13:32
L7	42	adjust\$4 near3 core adj clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 13:00
L9	110	core adj clock with (delay dll\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 13:32
L10	34	core adj clock with (delay dll\$1) same synchroniz\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 14:00
L11	48	core adj clock with (delay\$3) same synchroniz\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 14:00
L12	31	core adj clock with (delay\$3) with synchroniz\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 14:18
L13	1	core adj clock with slave adj2 (delay dll\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 14:18

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L14	3	core adj clock same slave adj2 (delay dll\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 14:24
L15	7	slave adj2 (delay dll\$1) with clock\$3 with tree	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/18 14:25
S1	127	memory near3 interface same i adj o adj buffer\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/17 16:03
S2	64	memory near3 interface same i adj o adj buffer\$1 same signals	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/17 16:09
S3	321	slave adj2 (delay dll\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/17 16:09
S4	1	S2 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/17 16:09
S5	28	memory near3 interface and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/17 16:10